## REPLY UNDER 37 C.F.R. § 1.116 EXPEDITED PROCEDURE EXAMINING GROUP 2182

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/690,266 Filed: May 20, 2004 Inventor(s): Dominik J. Schmidt		* * * * * * * *	Examiner: Group/Art Unit: Atty. Dkt. No: Conf. No.	Phan, Dean 2182 6057-61200 4584
Title:	INTEGRATED CIRCUIT WITH A PLURALITY OF HOST PROCESSOR FAMILY TYPES	***		

## RESPONSE TO FINAL OFFICE ACTION OF July 18, 2007

## **MAIL STOP AF**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of July 18, 2007, to further highlight why the application is in condition for allowance.